

FIG.1

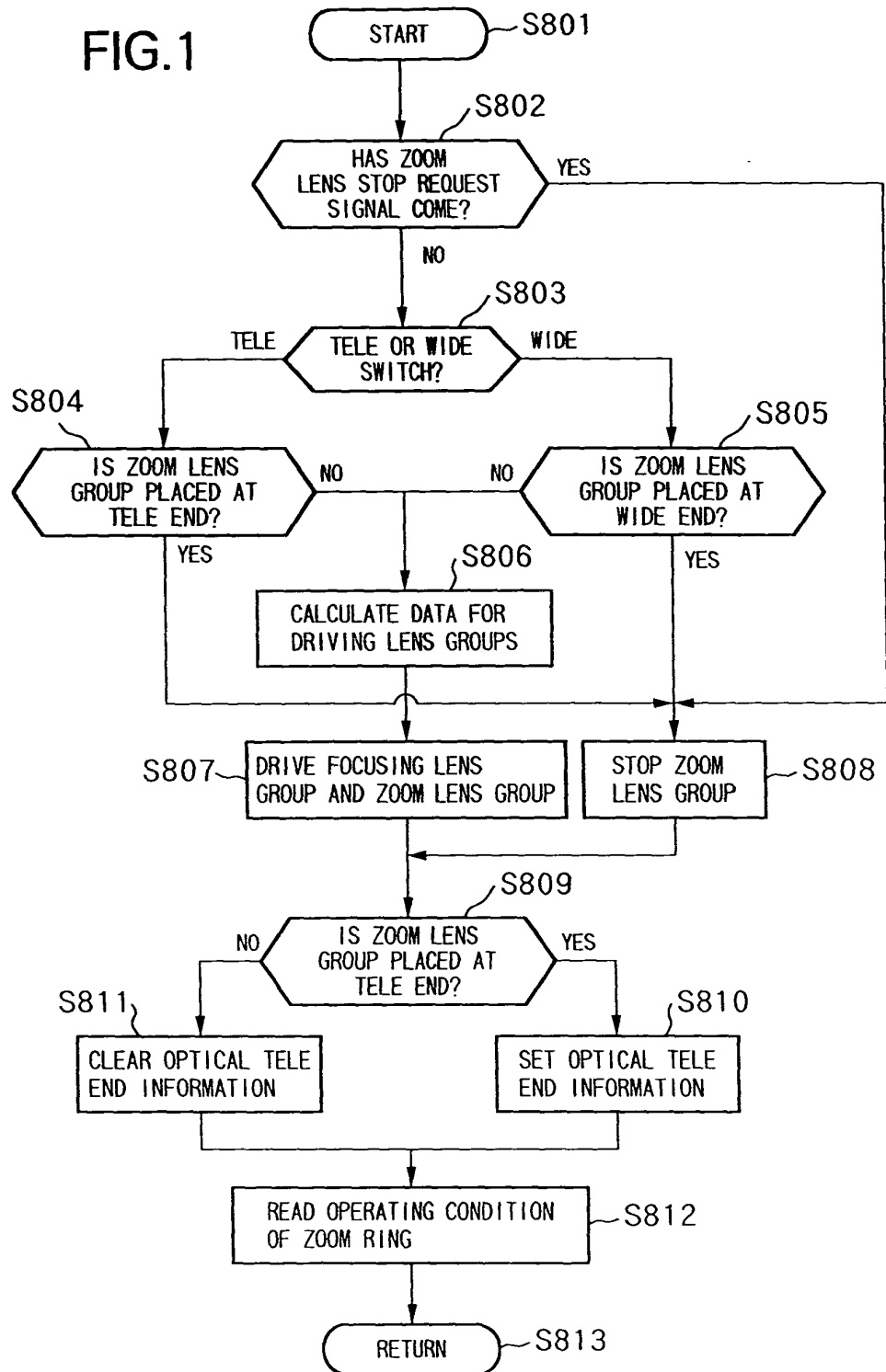


FIG.2

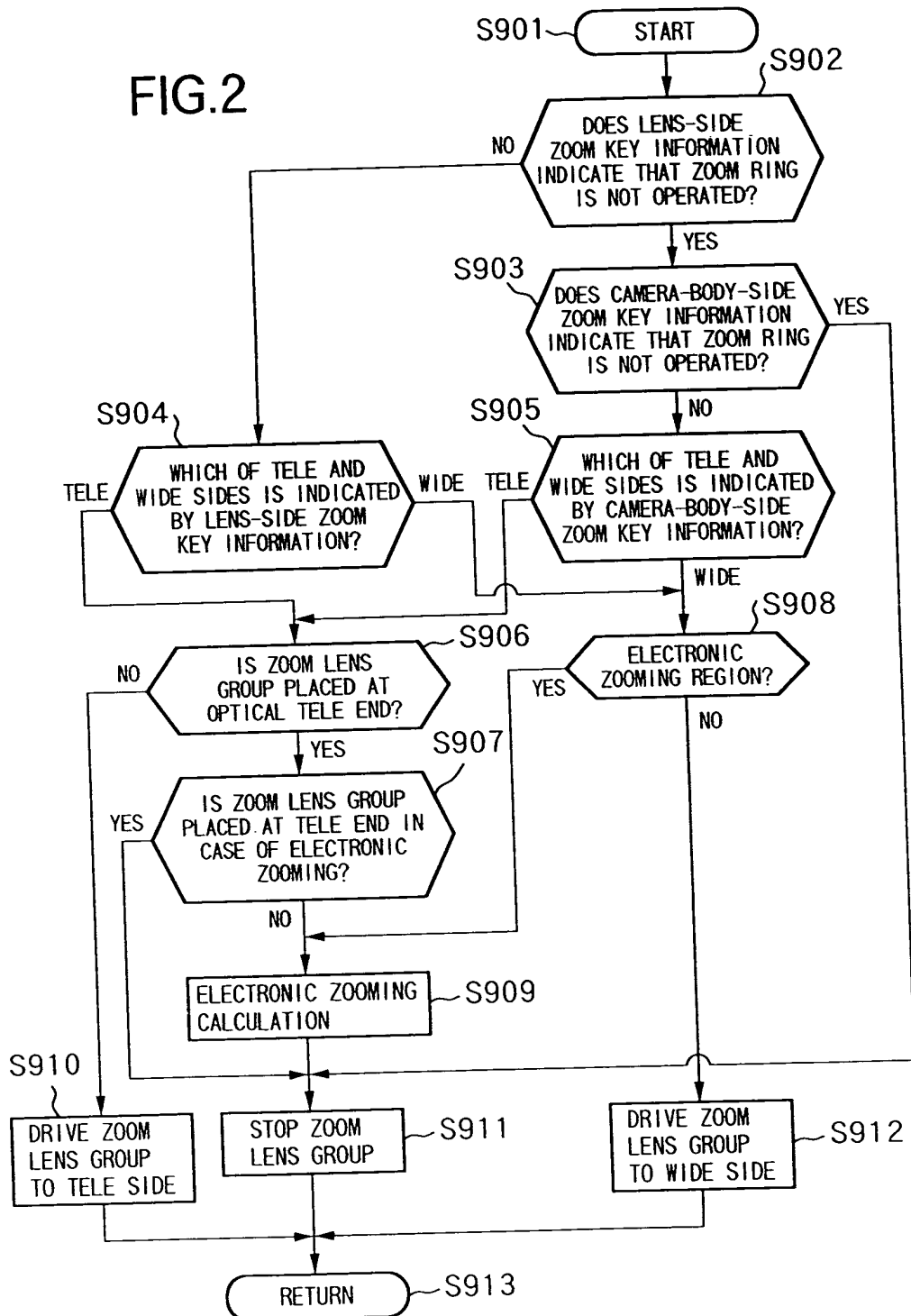


FIG.3

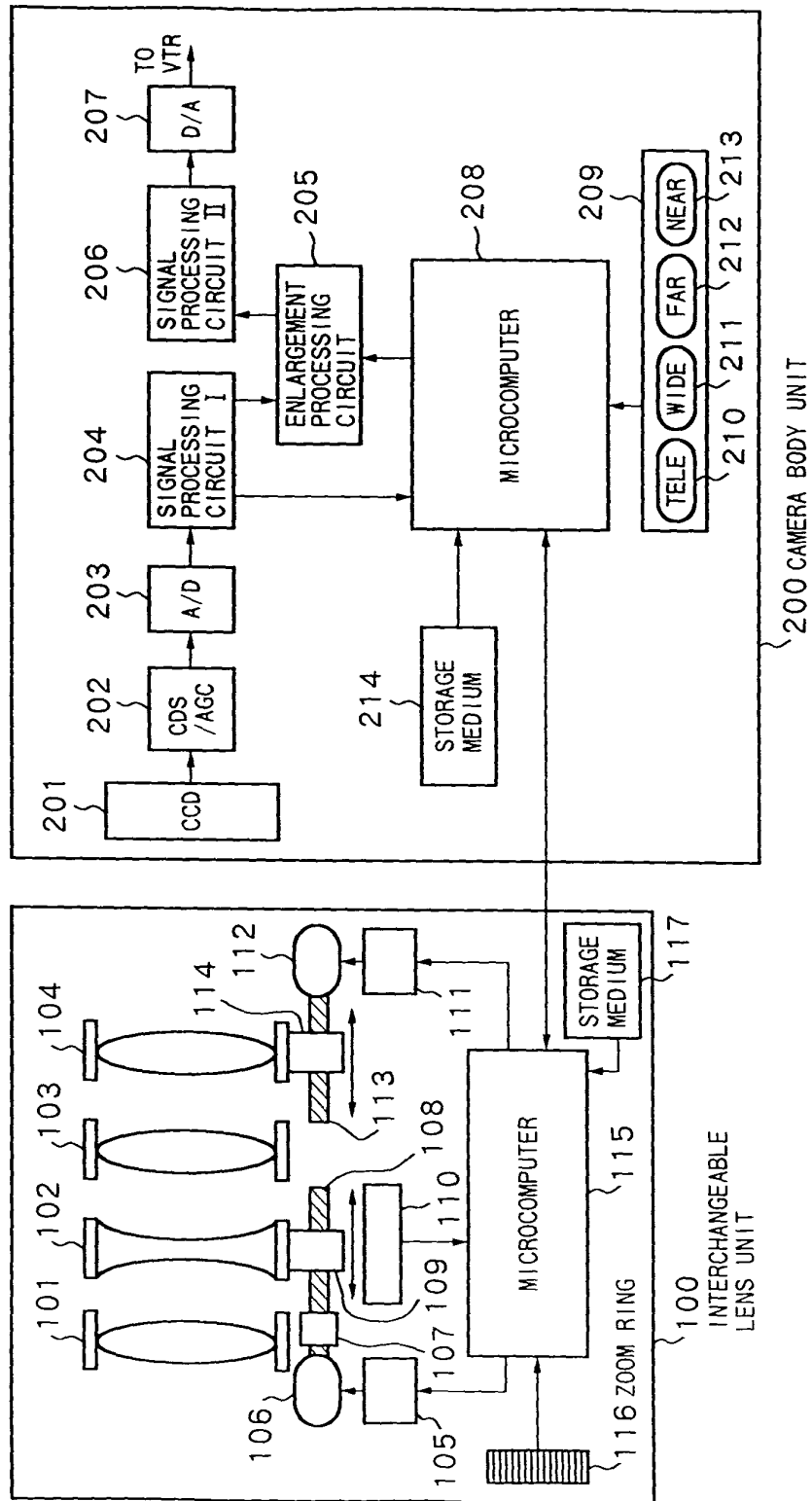
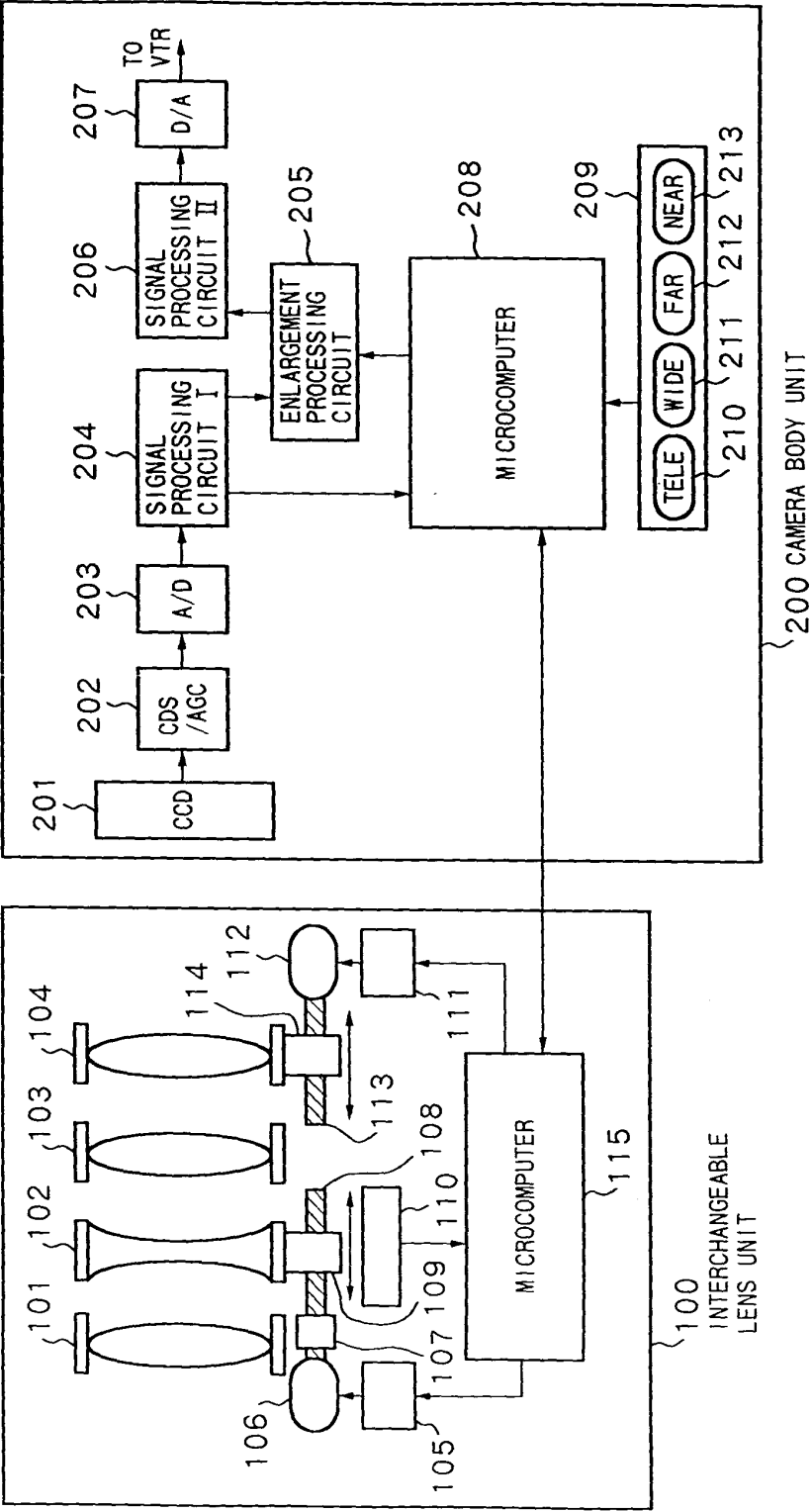


FIG.4



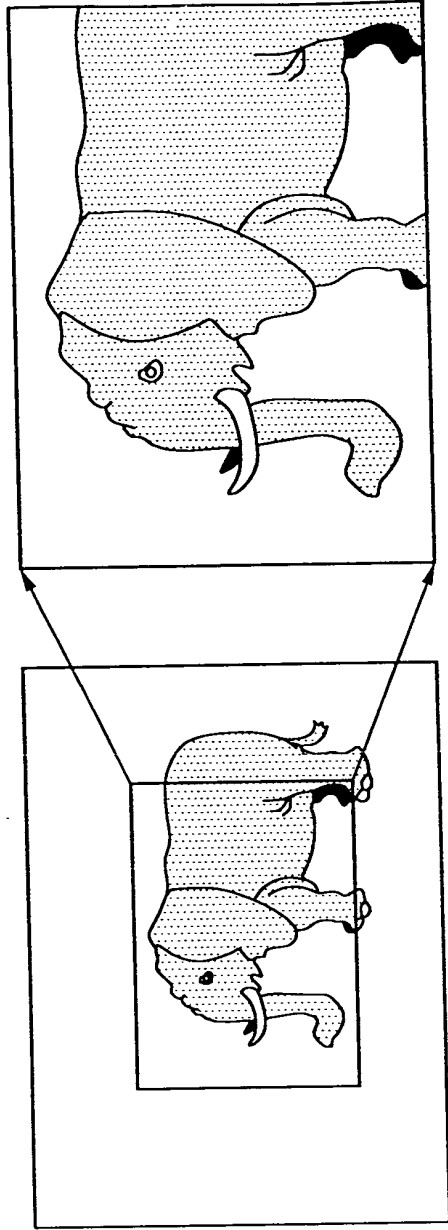


FIG. 5A

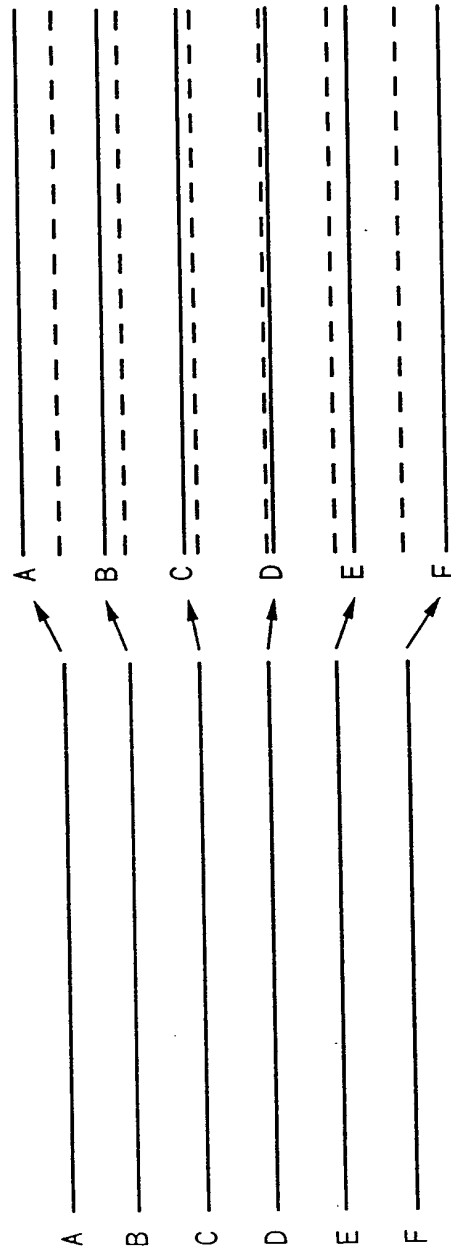


FIG. 5B

FIG.6

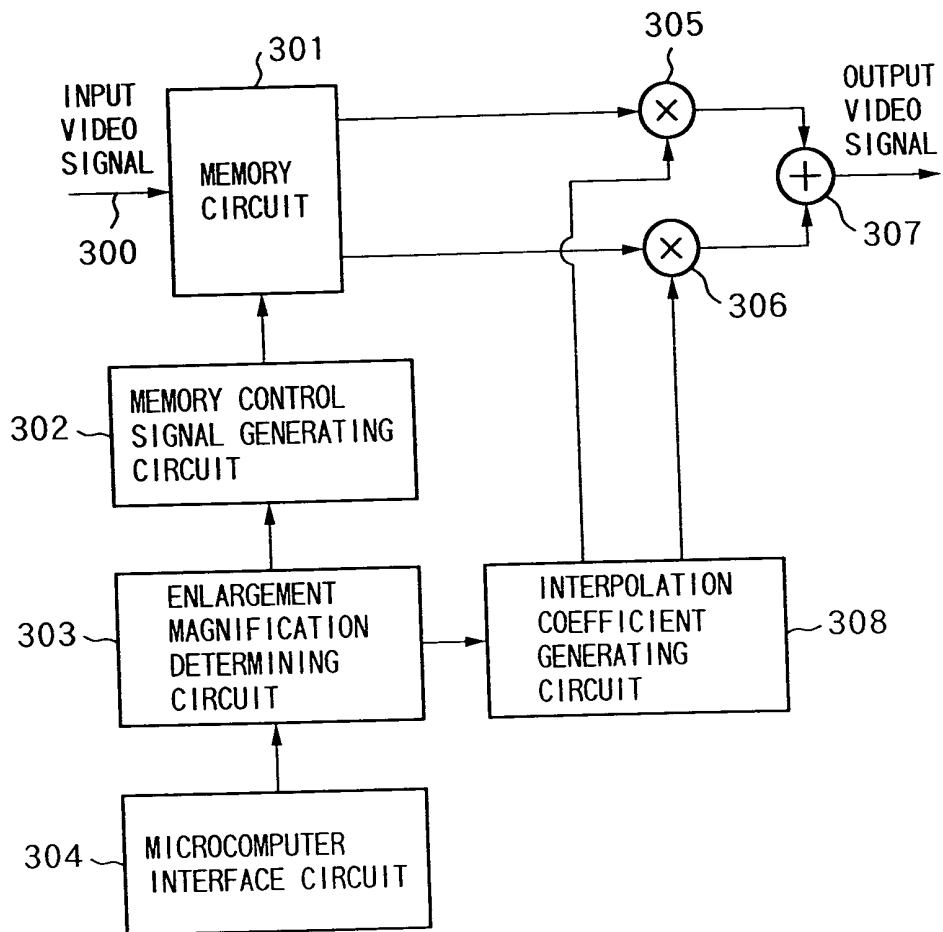


FIG.7

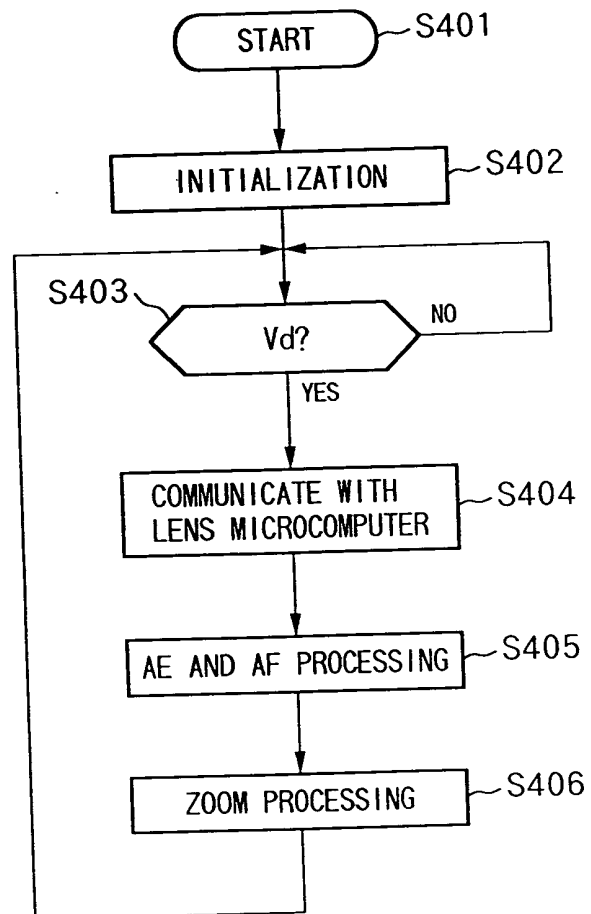


FIG.8

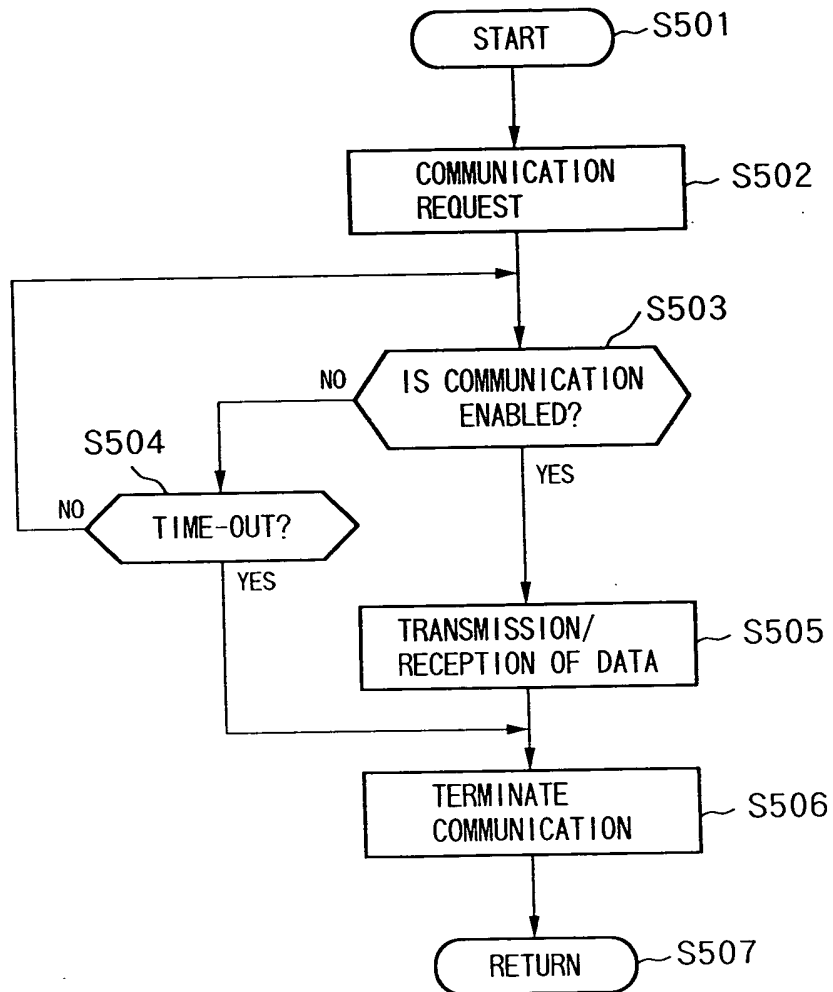


FIG.9

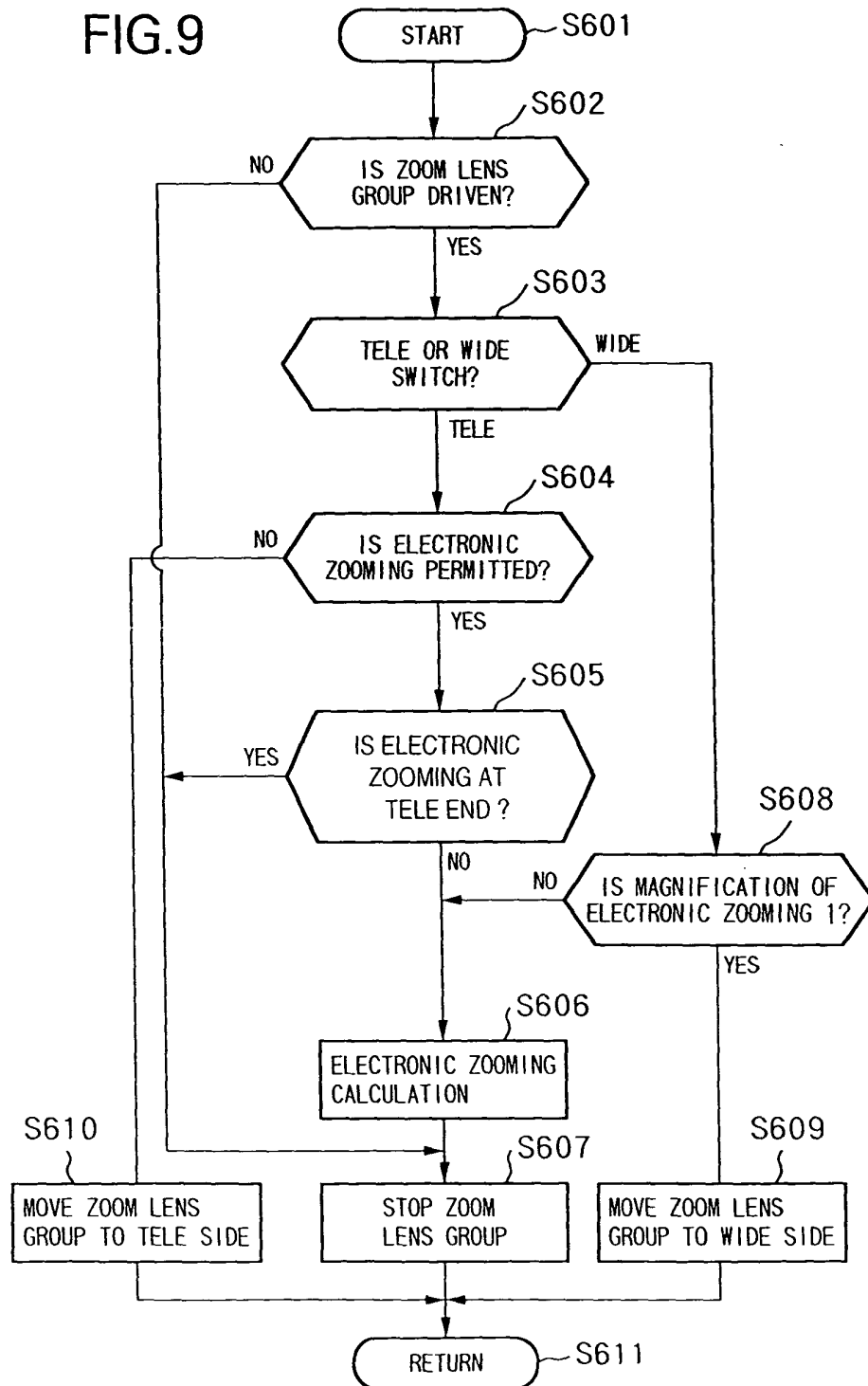


FIG.10

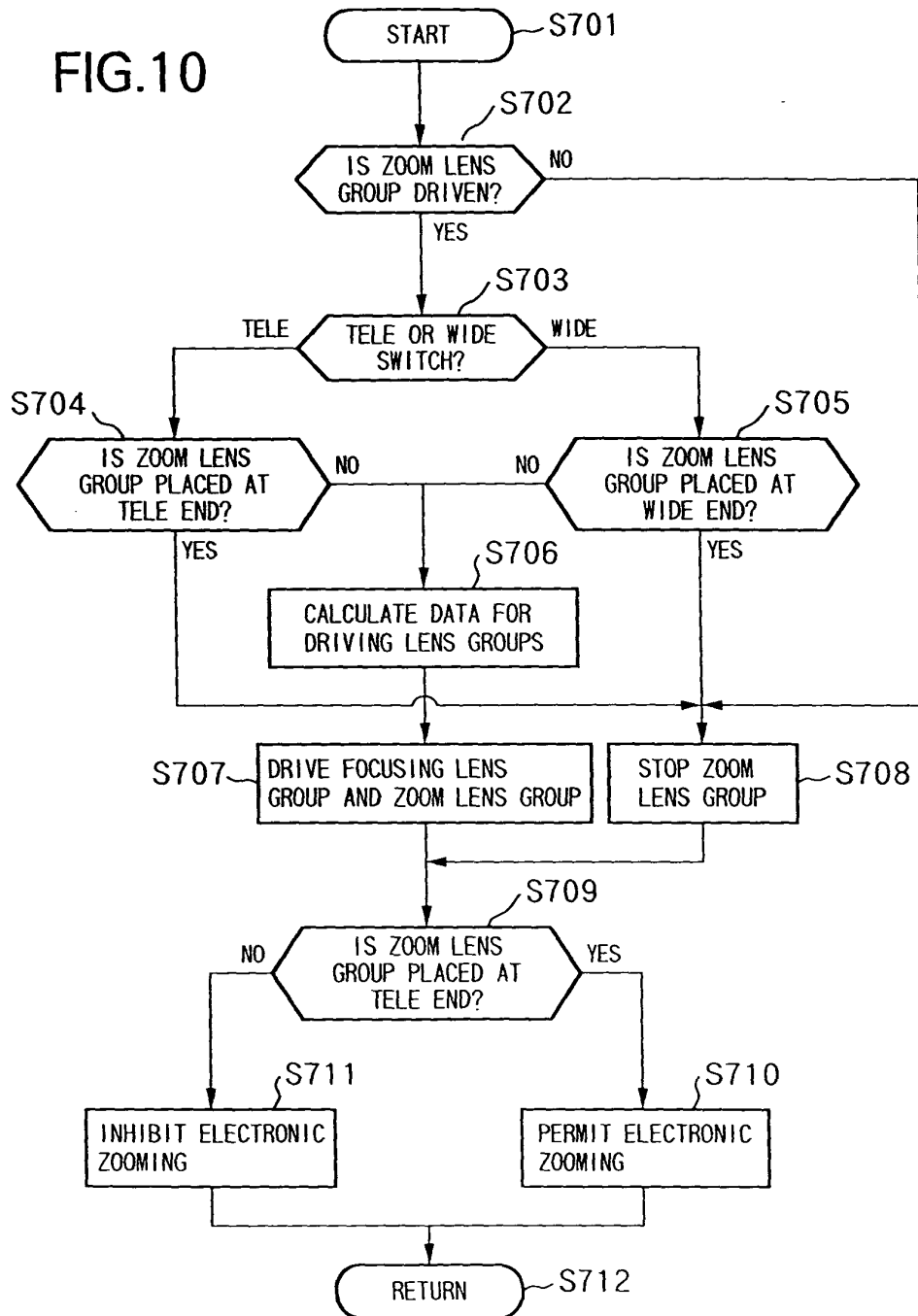


FIG.11

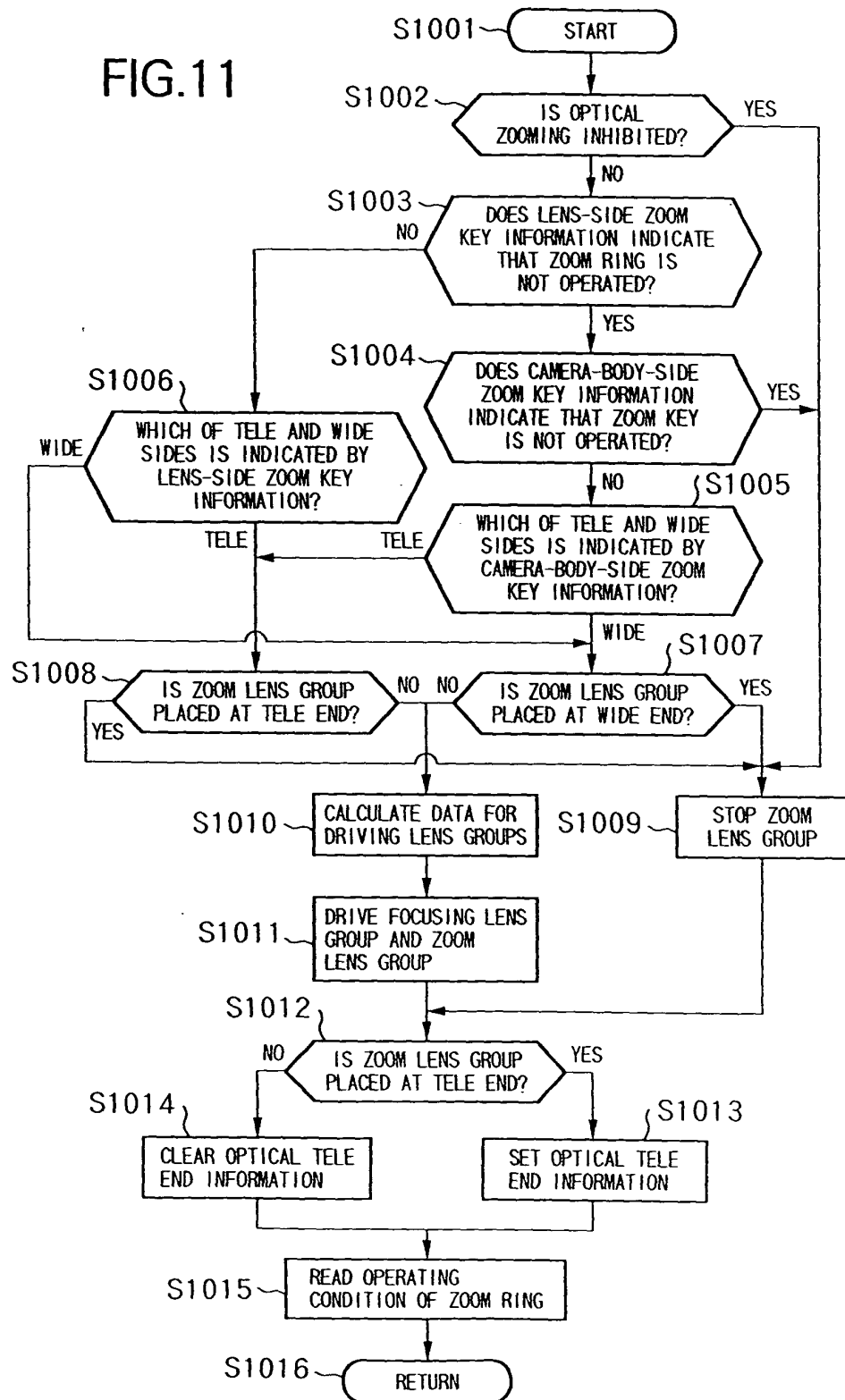
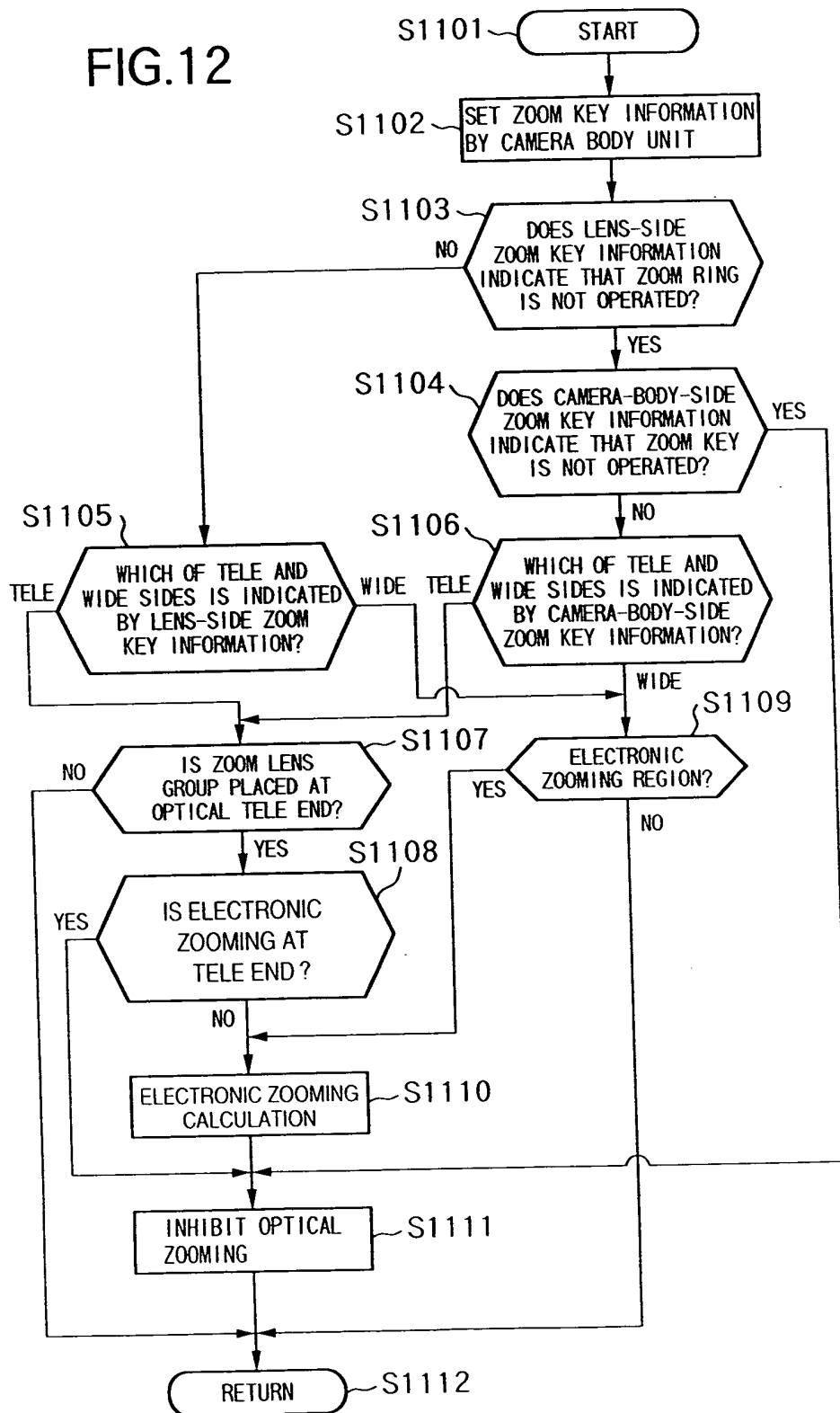


FIG.12



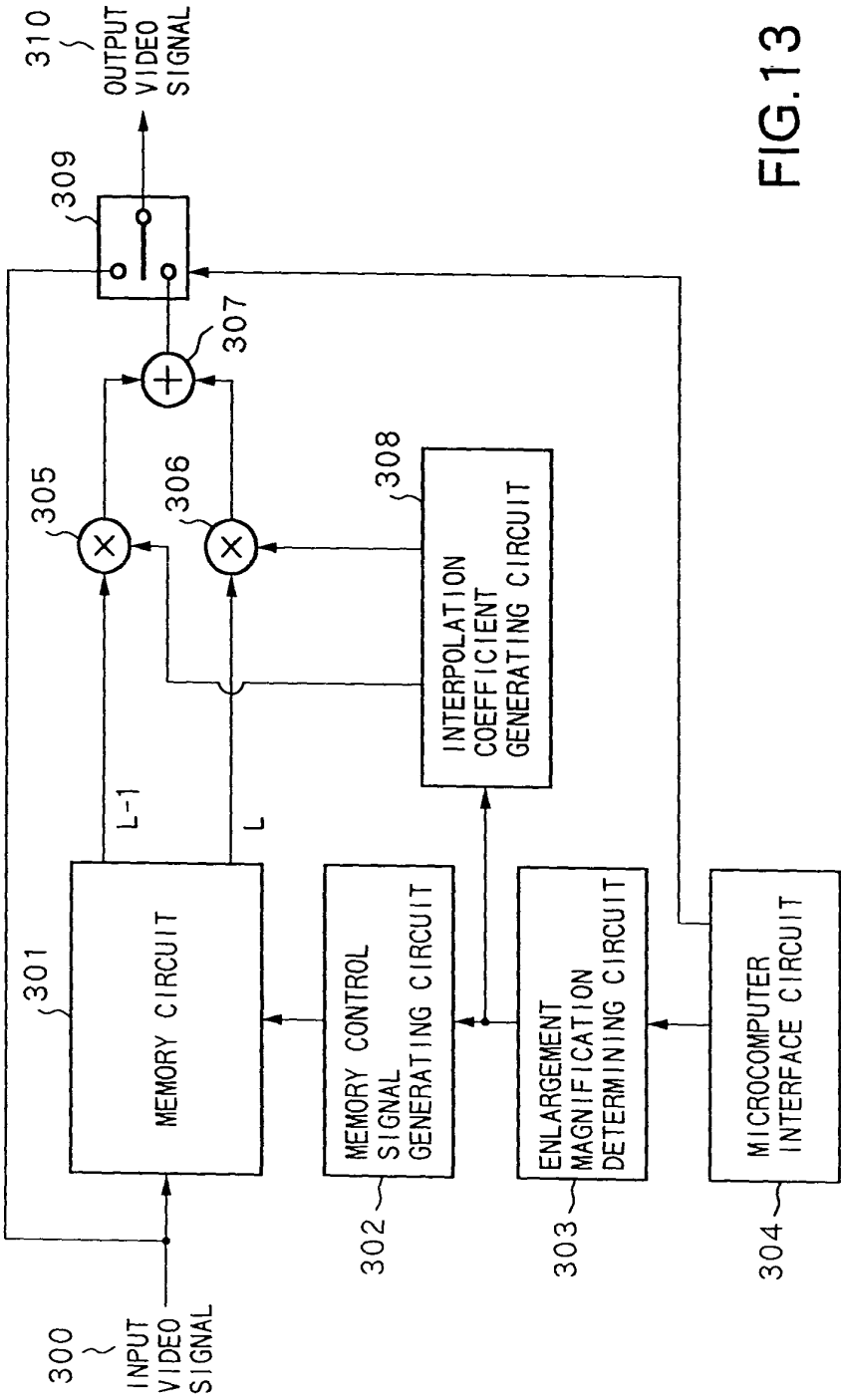


FIG.13

FIG. 14

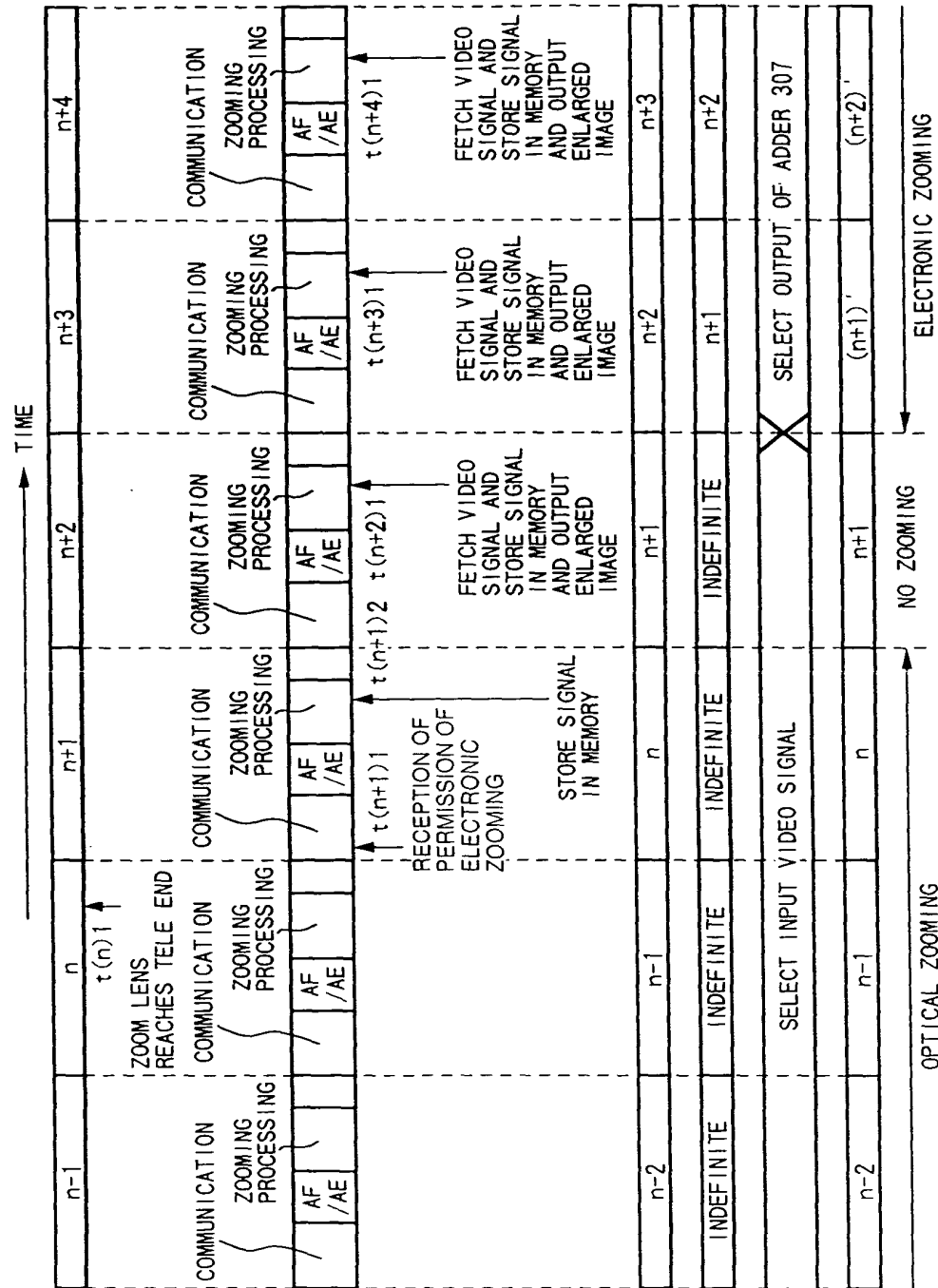
801
FIELD802
PROCESSING TO
BE PERFORMED
BY CAMERA
MICROCOMPUTER
208803
INPUT VIDEO
SIGNAL 300804
OUTPUT OF
ADDER 307805
OUTPUT SWITCH
SIGNAL806
OUTPUT VIDEO
SIGNAL 310

FIG.15

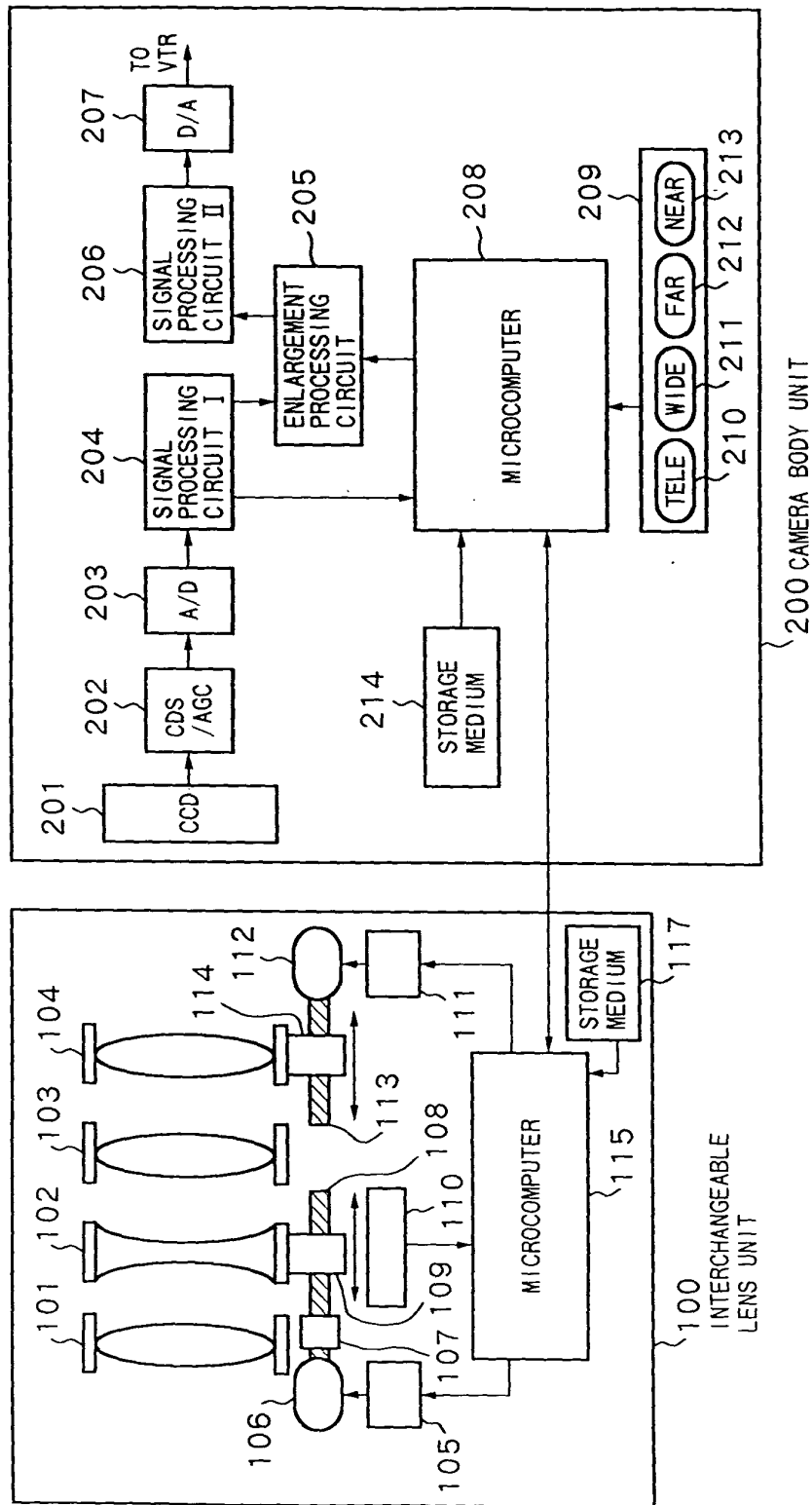


FIG.16

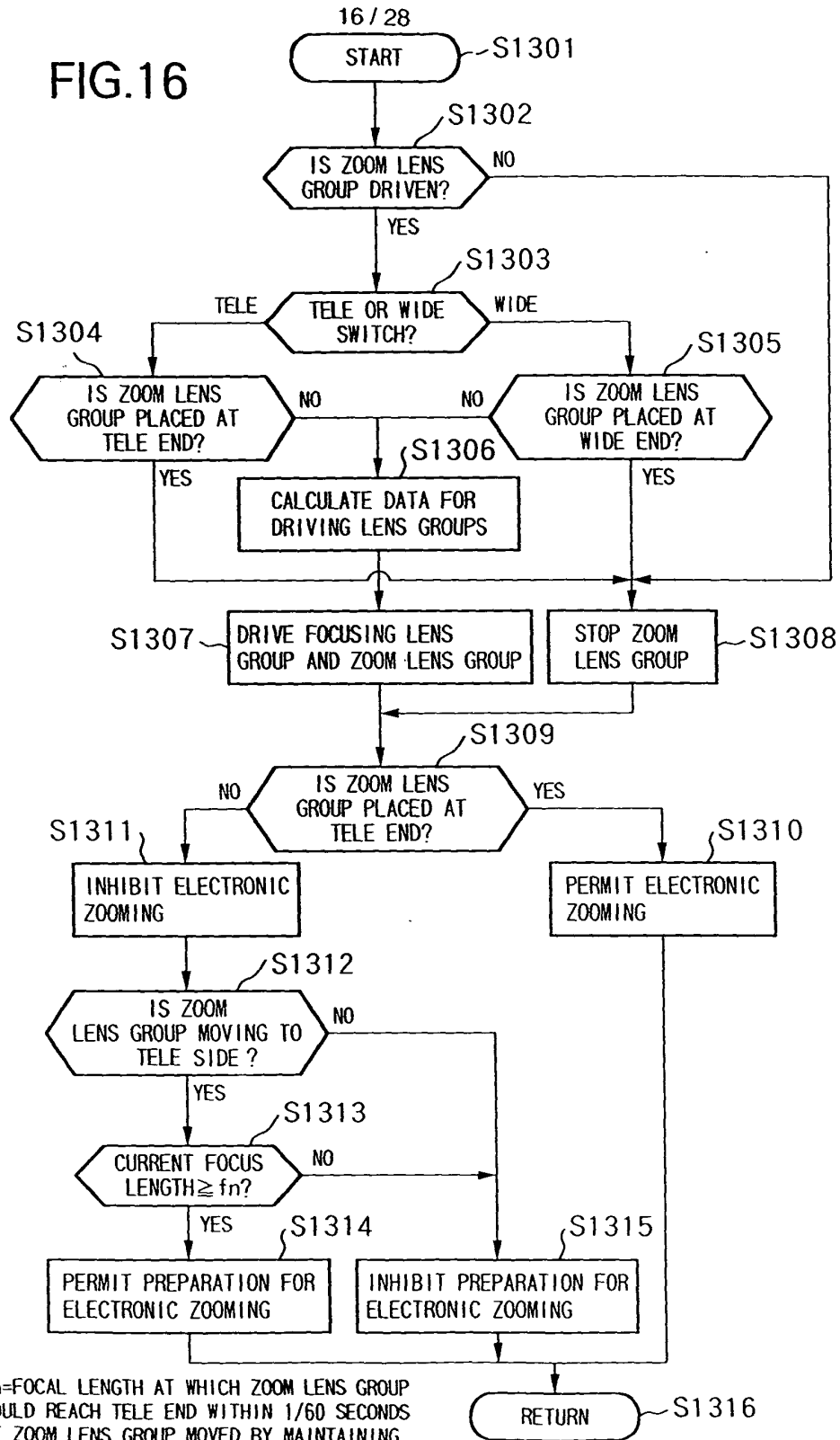


FIG. 17

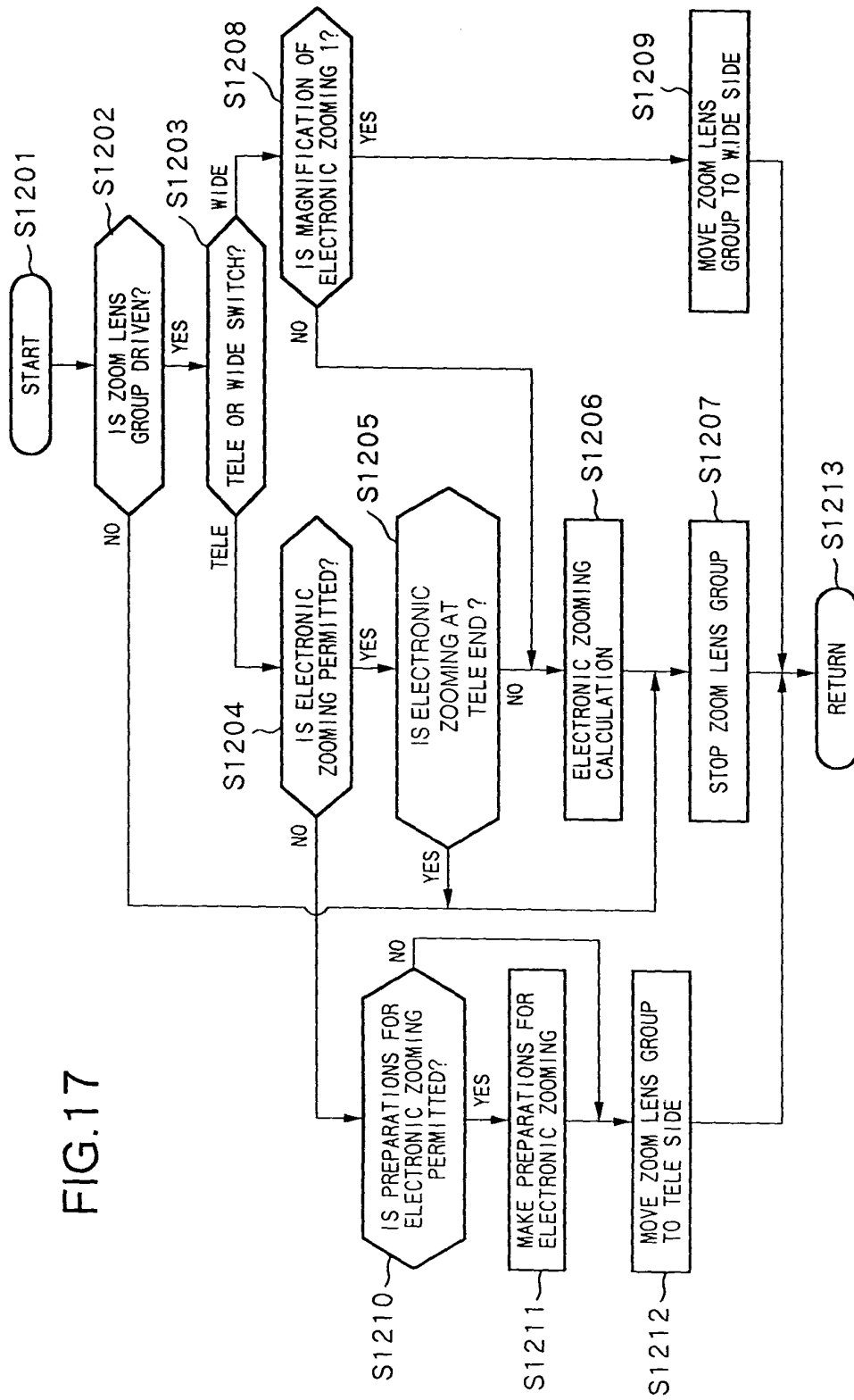


FIG.18

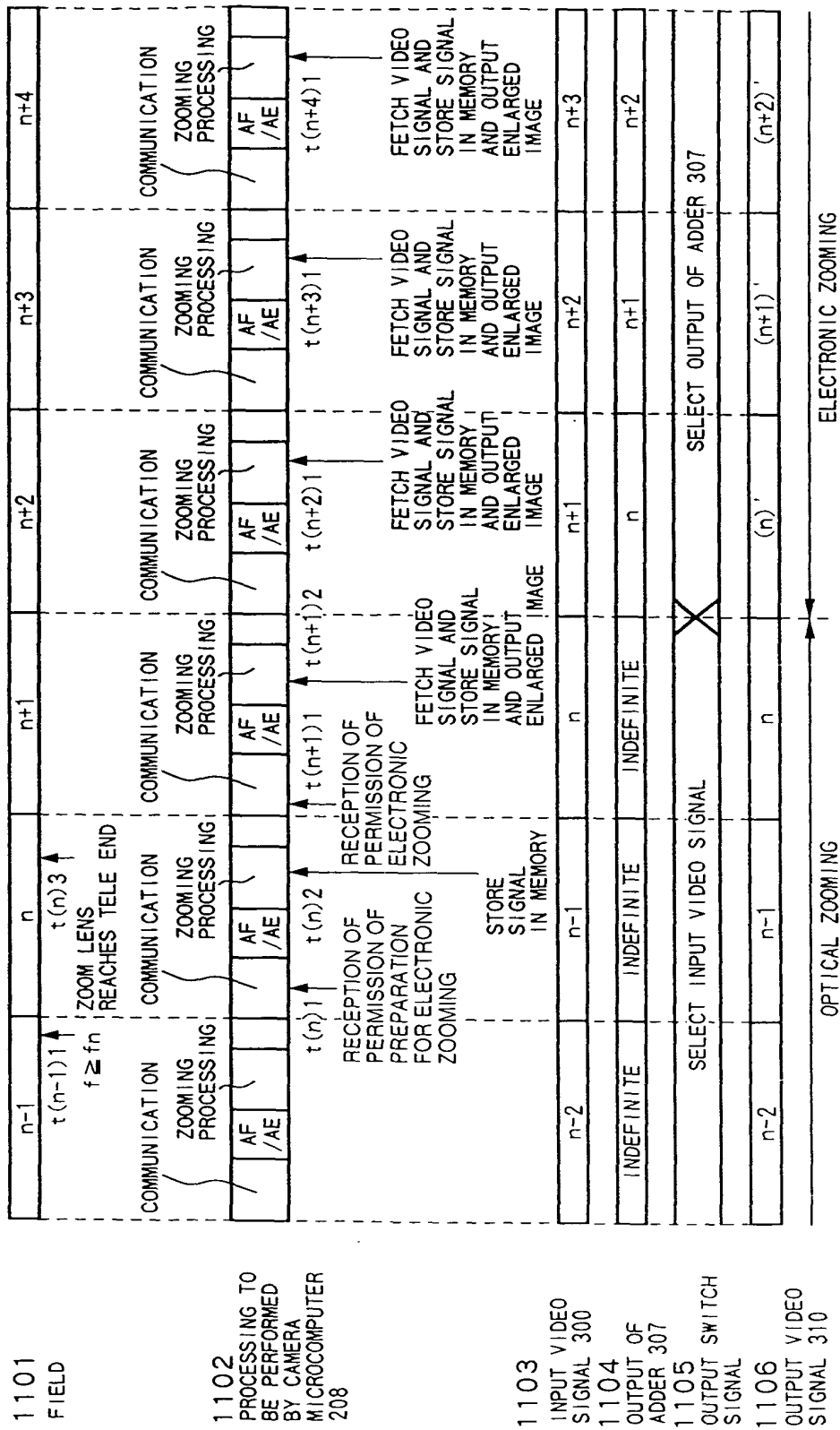


FIG.19

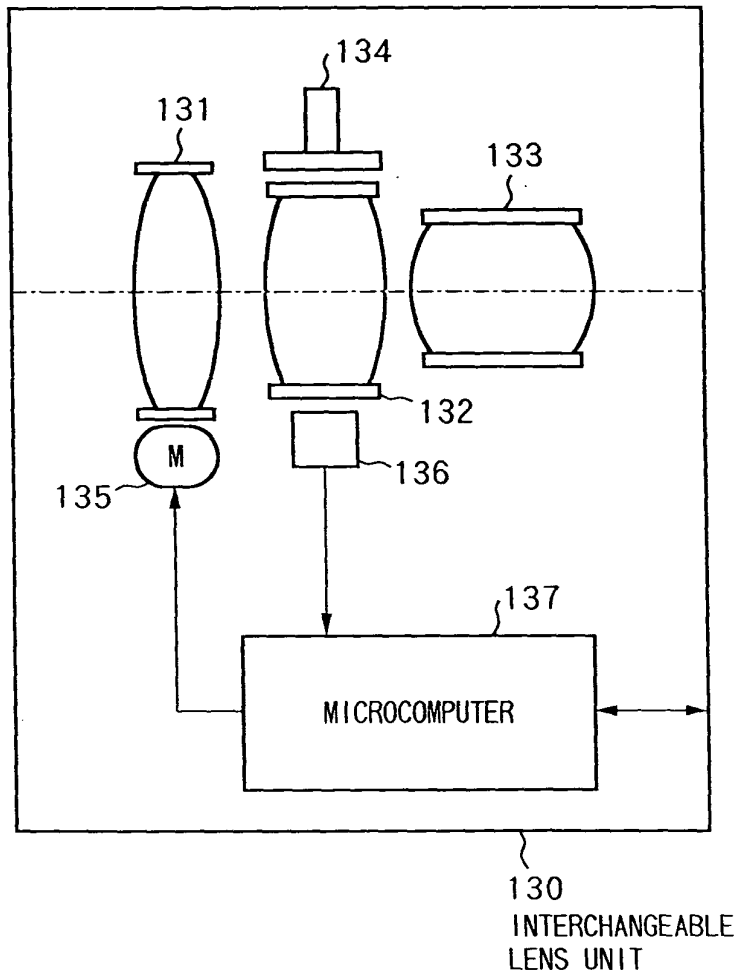


FIG.20

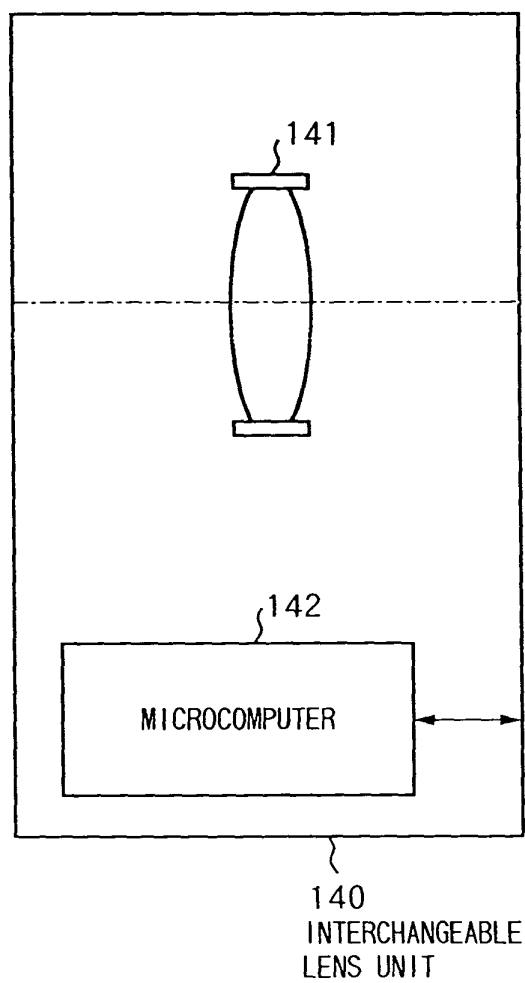


FIG.21

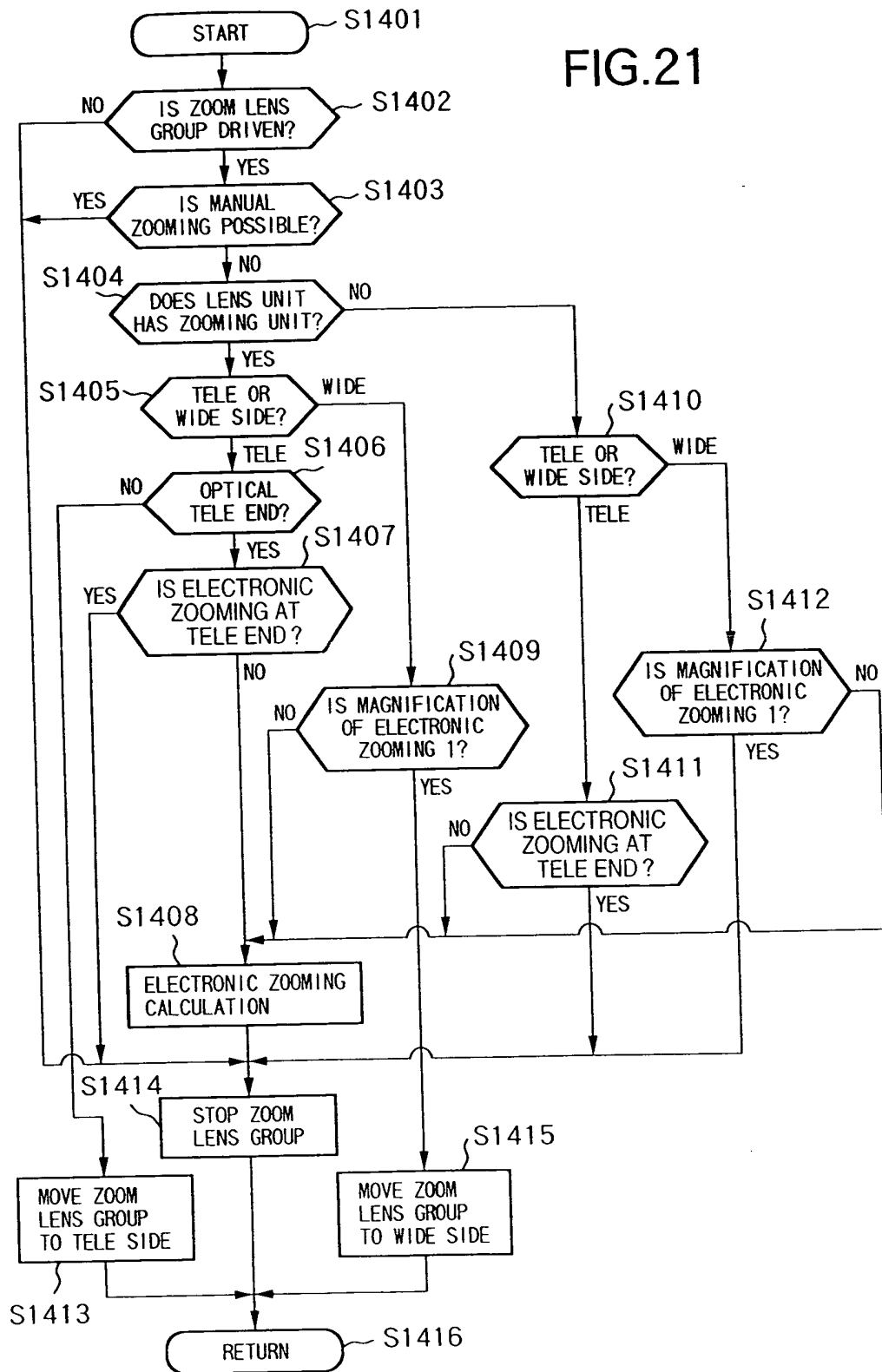


FIG.22

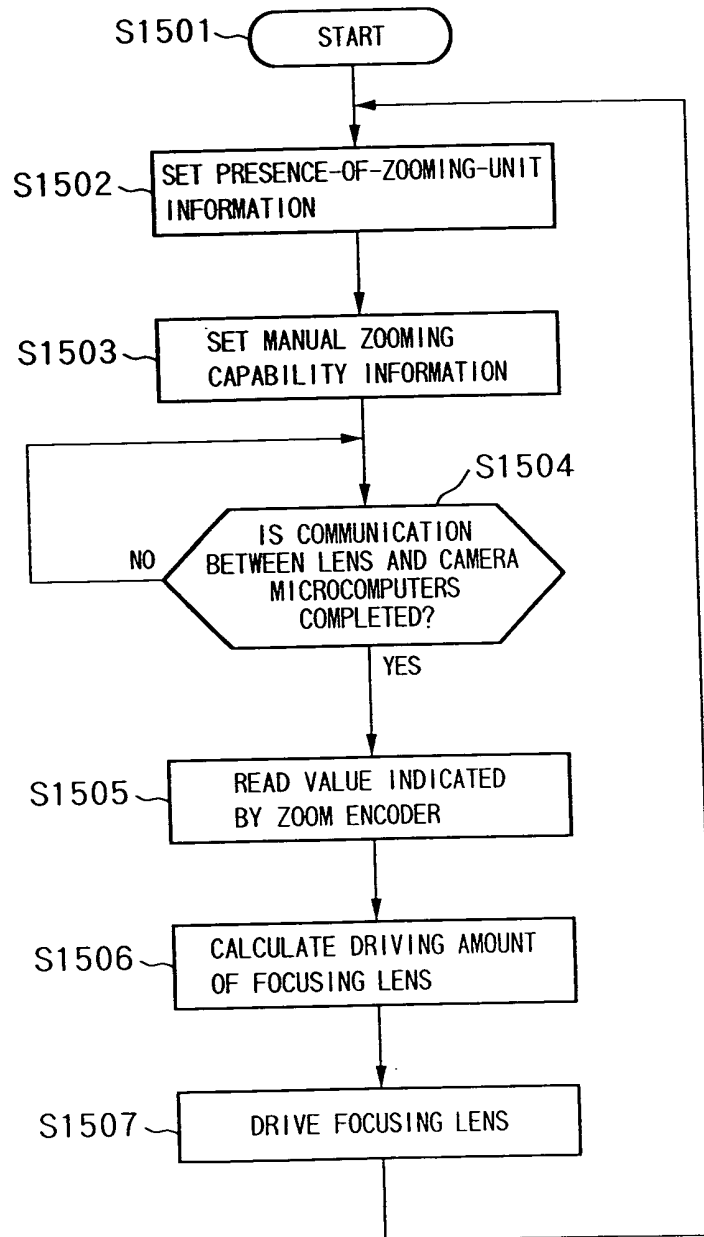


FIG.23

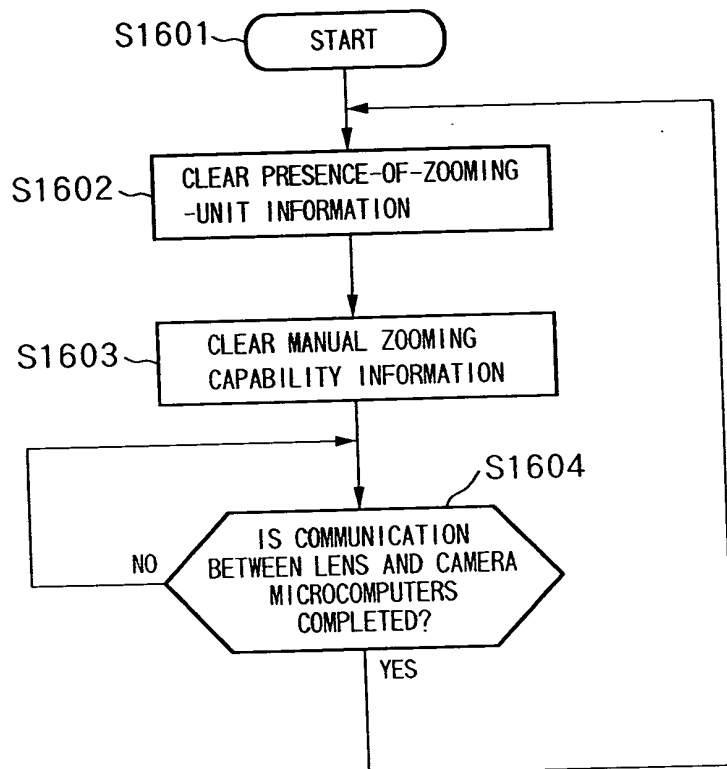


FIG.24

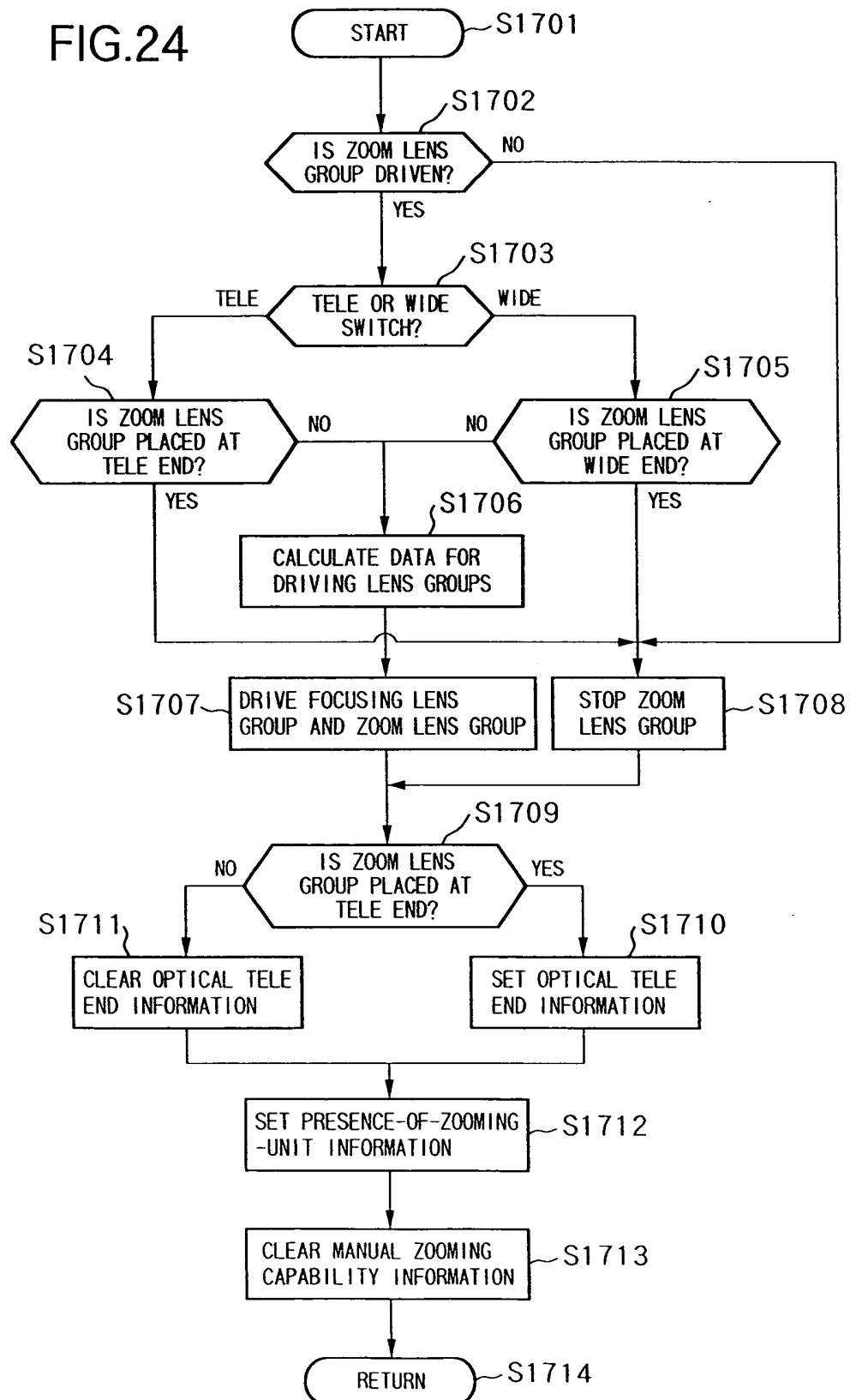


FIG.25

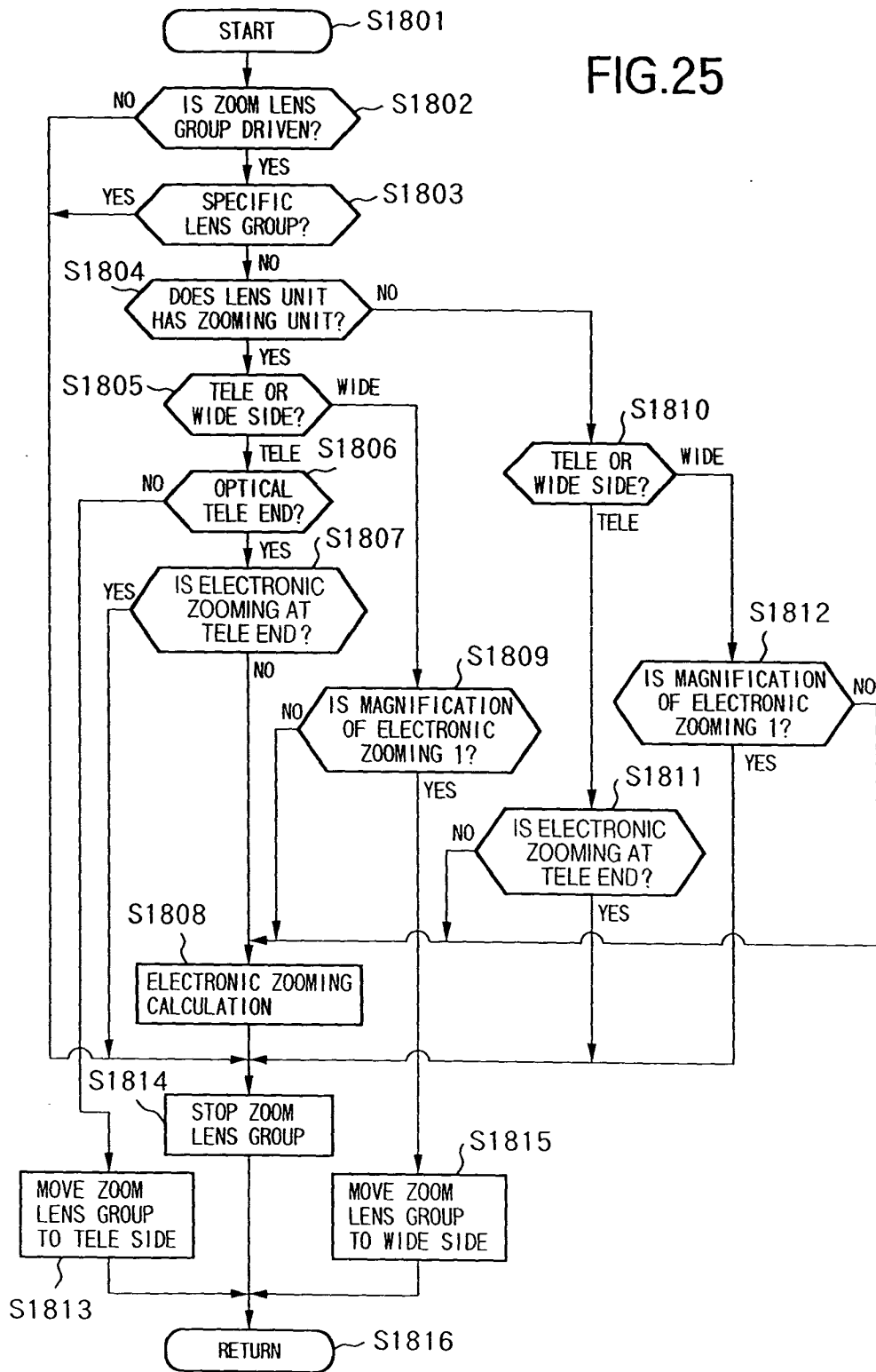


FIG.26

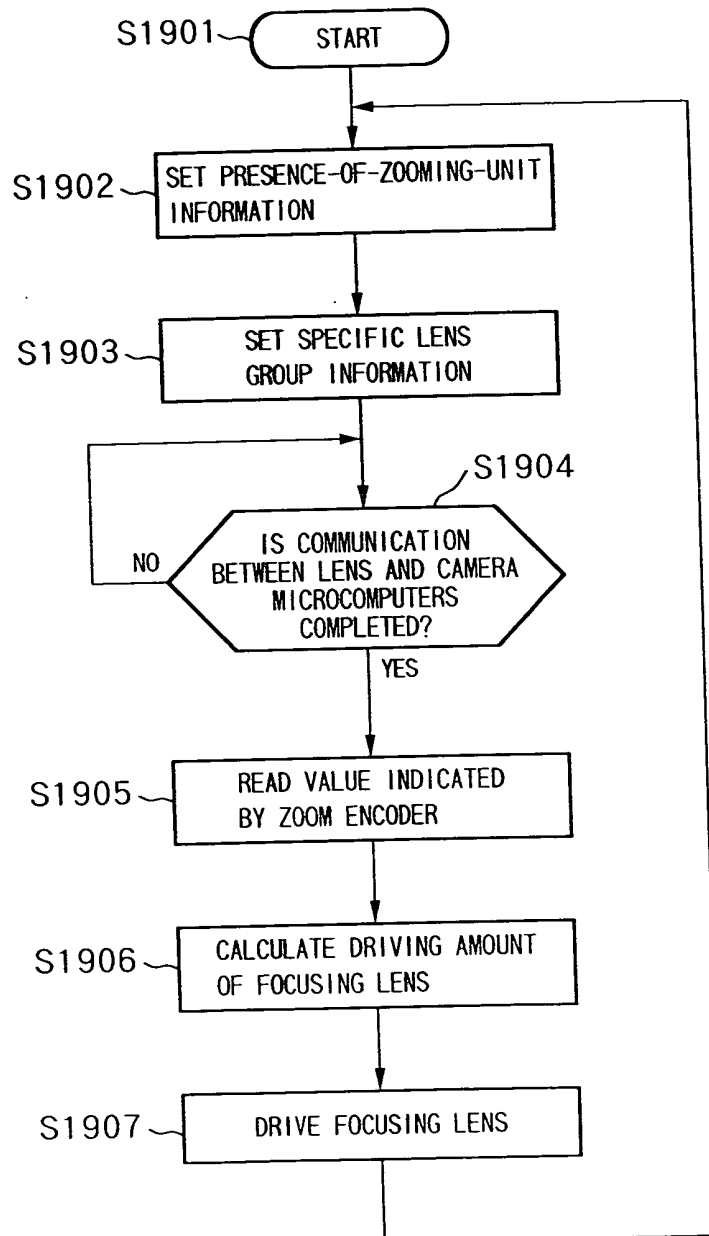


FIG.27

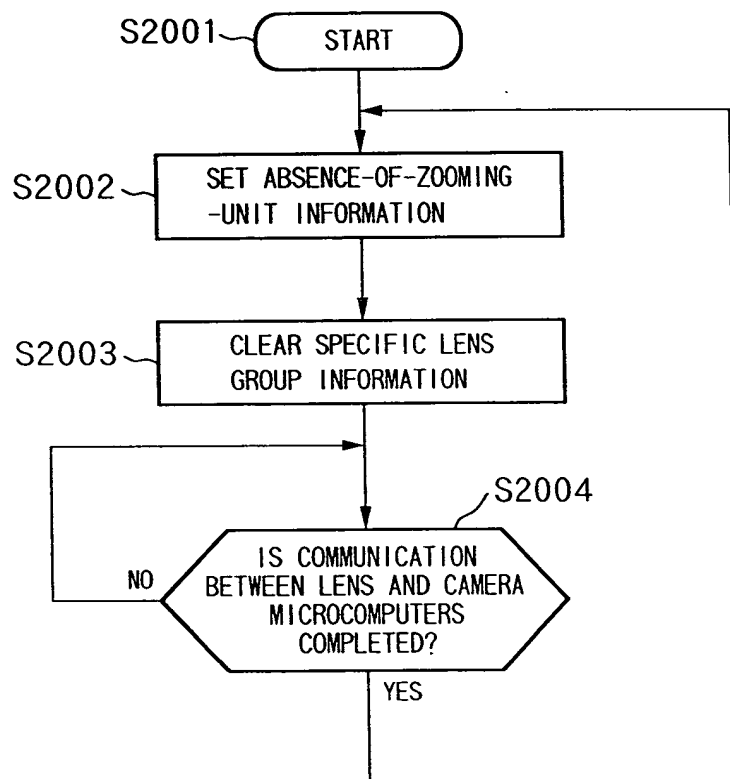


FIG.28

